

RTSS 2006 - Workshop on Models and Analysis for Automotive Systems

Final Program

8:30 - 8:50	Registration	
8:50 - 9:00	Welcome and opening	
Session I - System Design		
9:00 - 9:10	5 D. Santos, A Frolich	CAN development according to Application-Oriented System Design
9:10 - 9:20	10 K. Chaaban, P. Crubille', M. Shawky	A Survey On Software Architecture And Methodology Design For Embedded Automotive Systems: The SCOOT-R example
9:20 - 9:30	11 G. Raravi, J. Bharadia, K. Ramamritham	Towards Intelligent Vehicles: Automatic Merge Control
9:30 - 9:45	Panel discussion	
Session II - Flexible Architectures		
9:45 - 9:55	3 F. Ataide, C. Pereira	A New Approach for Time-Triggered Phase in the FTT-CAN Protocol, A Case Study in an Automotive System
9:55 - 10:05	4 J. Helander, J. Preden	Adapting the Auto to a New Tune
10:05 - 10:15	9 M. Stilkerich et al	A Distributed Middleware for Automotive Applications
10:15 - 10:30	Panel discussion	
10:30 - 11:00	Coffee Break	
Session III - Component-Based SW Engineering		
11:00 - 11:10	2 M. Akerholm et al	Handling Subsystems using the SaveComp Component Technology
11:10 - 11:20	15 J. Medina et al.	A Modeling Approach for the Timing Verification of COTS Components-based Distributed Hard Real-Time Systems
11:20 - 11:30	12 J. Fredriksson, T. Nolte, M. Nolin, H. Schmidt	Predicting Execution-Time for Variable Behaviour Embedded Real-Time Components
11:30 - 11:45	Panel discussion	
Session IV - Formal Methods and Model-driven Development		
11:45 - 11:55	6 Y. Boussemart, K. Lundqvist	The Gurkh Framework: an Industrial Case Study and Certification Issues for Safety-Critical Software
11:55 - 12:05	7 J. DeAntoni, JP Babau	SAIA, a model driven method for safe deployment of sensors based applications
12:05 - 12:15	13 Matteo Bordin, Marco Panunzio, Ondrej Rohlik	An Integrated Metamodel Driven Process Focusing on Reuse and Correctness
12:15 - 12:30	Panel discussion	
12:30 - 13:30	Lunch break	
Session V - AUTOSAR		
13:30 - 14:00	8 R. Racu, R. Ernst, K. Richter	The Need of a Timing Model for the AUTOSAR Software Standard
14:00 - 14:20	Discussion	
14:20 - 14:30	Closing remarks	
14:30 - 15:00	Break	
15:00 - 18:00	Students Design Competition CiberMouse@RTSS2006	