

# PAGING EXAMPLE

Take page = 4 bytes =  $2^2$

Take logical address space = 16 bytes =  $2^4$

Take physical memory = 32 bytes = 8 pages  
frames

⇒ logical address:

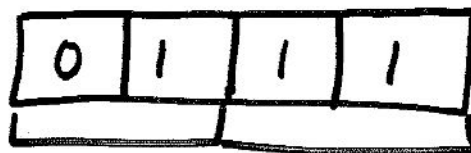


m - n  
4 - 2

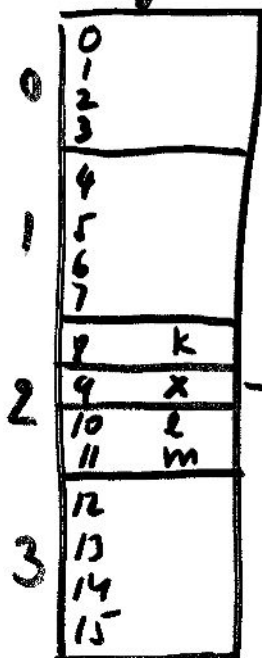
consists of  
2 bits representing  
page number

2 bits representing  
page offset

Example: Page 1, Offset 3 will be represented

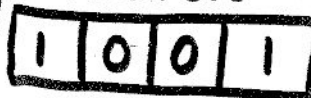


logical m.      P      d

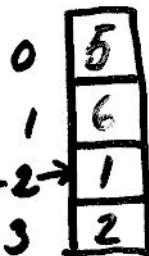


take log. address

$$9 = 1$$



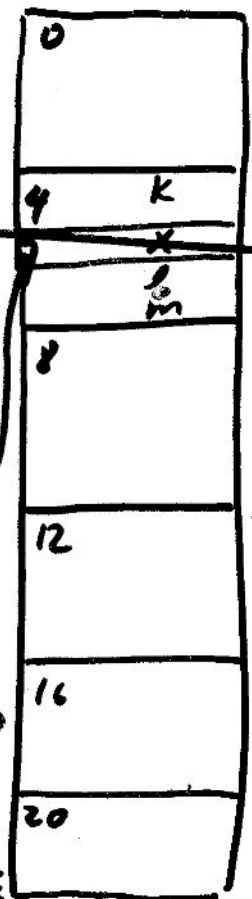
page 2  
offset 1



page table

$$(1 \times 4) + 1 = 5$$

phys. address



Physical memory